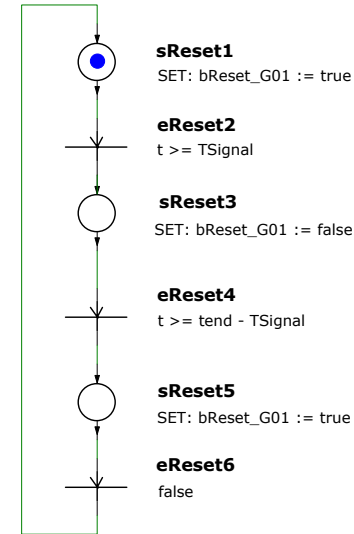


ResetGroup_G01



InitialValueChannel_C01

ICA :

fSignal := 440
 TSignal := 1/fSignal
 tend := 10*TSignal
 hmax := TSignal/20
 hmin := hmax
 ASignal := 3*sqrt(2)
 phiSignal := 45
 a0Signal := 1.2*ASignal

SigA_C01

Frequenz := fSignal
 Periode := TSignal
 Amplitude := ASignal
 Phase := phiSignal
 periodisch := j
 Offset := a0Signal

xDc1y1df1

xDc1y -> df
 ADuP Sol. SMF

b_reset := bReset_G01
 k_character := 2
 c_Dx := h
 c_x := t
 c_y := SigA_C01
 d_f := d_f
 bj_synch := bj_synch
 e_stamp := e_stamp
 i_CA := i_CA
 p_y := p_y

OutputInstance_I01

VA2 :

tsw := h
 uf_I01 := d_f - fSignal
 vf_I01 := squ(uf_I01/fSignal)

Core:

ADuP_So1

Nr.:

T01-f1110

Input:

xDc1y

Output:

df

Version:

SMF 4.2.1

