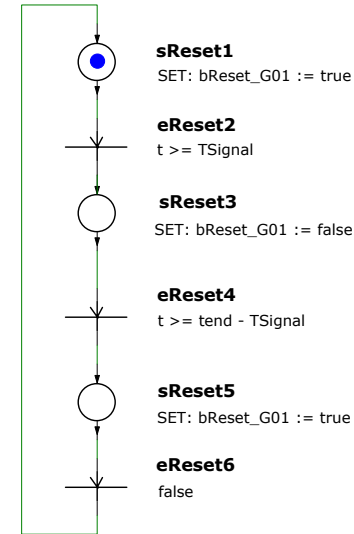


ResetGroup_G01



xDc1y1dCL1

xDc1y -> dCL
ADuP So1 SMF

b__reset := bReset_G01
k__character := 2
c__Dx := h
c__x := t
c__y := SigA_C01
d__f := d__f

bLj__synch := bLj__synch
bRj__synch := bRj__synch
bj__synch := bj__synch
eL__stamp := eL__stamp
eR__stamp := eR__stamp
e__stamp := e__stamp
m__y_n := m__y_n
m__y_m := m__y_m
m__y_mn := m__y_mn
d__y_cn := d__y_cn
d__y_cm := d__y_cm
i__CA := i__CA
p__T := p__T
p__y_0 := p__y_0
p__y := p__y

InitialValueChannel_C01

ICA :

RMS_AC_Part := 3
fSignal := 440
TSignal := 1/fSignal
tend := 10*TSignal
hmax := TSignal/20
hmin := hmax
ASignal := sqrt(2)*RMS_AC_Part
phiSignal := 45
a0Signal := 0*ASignal
RMS_Value := sqrt(RMS_AC_Part*RMS_AC_Part + a0Signal*a0Signal)

SigA_C01

Frequenz := fSignal
Periode := TSignal
Amplitude := ASignal
Phase := phiSignal
periodisch := j
Offset := a0Signal

OutputInstance_I01

VA2 :

tsw := h
uCFm_I01 := d__y_cm - sqrt(2)
vCFm_I01 := squ(uCFm_I01/sqrt(2))
uCFn_I01 := d__y_cn - sqrt(2)
vCFn_I01 := squ(uCFn_I01/sqrt(2))

Core: **ADuP_So1**

Sheet: **T01-f1151**

Input: **xDc1y**

Output: **dCL**

Version: **SMF 4.2.1**