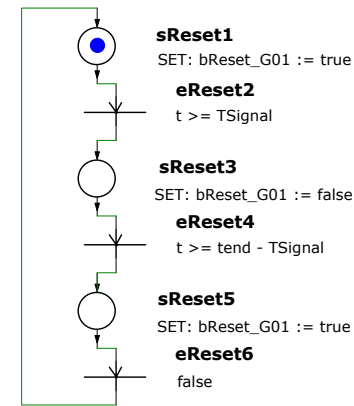


## ResetGroup\_G01



## xDc1y2cFT1

**xDc1y -> cFT**  
 ADuP So2 SMF  
 b\_reset := bReset\_G01  
 k\_character := 2  
 c\_Dx := h  
 c\_x := t  
 c\_y := cSignal\_C01  
 d\_f := d\_f  
 blj\_synth := blj\_synth  
 brj\_synth := brj\_synth  
 bj\_synth := bj\_synth  
 eL\_stamp := eL\_stamp  
 eR\_stamp := eR\_stamp  
 e\_stamp := e\_stamp  
 c\_y\_0 := c\_y\_0  
 c\_y\_y0 := c\_y\_y0  
 d\_y\_1c := d\_y\_1c  
 d\_y\_1d := d\_y\_1d  
 c\_y\_1 := c\_y\_1  
 c\_y\_y01 := c\_y\_y01  
 i\_CA := i\_CA  
 p\_T := p\_T  
 p\_y\_0 := p\_y\_0  
 p\_y := p\_y

## InitialValueChannel\_C01

**ICA :**  
 fSignal := 440  
 TSignal := 1/fSignal  
 tend := 10\*TSignal  
 hmax := TSignal/20  
 hmin := hmax  
 ASignal := 3\*sqrt(2)  
 phiSignal := 45  
 a0Signal := 0

## SigA\_C01

Frequenz := fSignal  
 Periode := TSignal  
 Amplitude := ASignal  
 Phase := phiSignal  
 periodisch := j  
 Offset := 0\*a0Signal

## SigB\_C01

Frequenz := 50/440\*fSignal  
 Periode := 440/50\*TSignal  
 Amplitude := 0.1p\*ASignal  
 Phase := phiSignal - 90  
 periodisch := j  
 Offset := a0Signal/2

## InputChannel\_C01

cSignal\_C01 := SigA\_C01 + SigB\_C01

## OutputInstance\_I01

**VA2 :**  
 tsw := h  
 uSigA\_I01 := c\_y\_1 - SigA\_C01  
 vSigA\_I01 := squ(uSigA\_I01/ASignal)  
 uSig\_I01 := c\_y\_y01  
 vSig\_I01 := squ(uSig\_I01/ASignal)

Core:

ADuP\_So2

Sheet:

T02-f1242

Input:

xDc1y

Output:

cFT

Version:

SMF 4.2.1

